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## TRANSMITTAL FORM

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Total Number of Pages in This Submission

Application Number	10/779,904-Conf. #9608
Filing Date	February 17, 2004
First Named Inventor	Masahiro Ishida
Art Unit	2133
Examiner Name	Not Yet Assigned
Total Number of Pages in This Submission	02008/071003

### ENCLOSURES (Check all that apply)

<input type="checkbox"/> Fee Transmittal Form	<input type="checkbox"/> Drawing(s)	<input type="checkbox"/> After Allowance Communication to TC
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<input type="checkbox"/> Amendment/Reply	<input type="checkbox"/> Petition	<input type="checkbox"/> Appeal Communication to TC (Appeal Notice, Brief, Reply Brief)
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### SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm Name	OSHA · LIANG LLP		
Signature			
Printed name	Thomas K. Scherer		
Date	January 20, 2006	Reg. No.	45,079

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the U.S. Postal Service as Express Mail, Airbill No. EV 766457872 US, on the date shown below in an envelope addressed to:  
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(Yuki Tsukuda)

Docket No.: 02008/071003  
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:  
Masahiro Ishida et al.

Application No.: 10/779,904

Confirmation No.: 9608

Filed: February 17, 2004

Art Unit: 2133

For: METHOD AND APPARATUS FOR DEFECT  
ANALYSIS OF SEMICONDUCTOR  
INTEGRATED CIRCUIT

Examiner: Not Yet Assigned

STATUS INQUIRY

22511

PATENT TRADEMARK OFFICE

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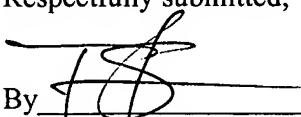
Dear Sir:

It is respectfully requested that the attorney named below be advised of the status of the above-identified application. Please advise us of when we might expect to receive an Office Action from the Patent and Trademark Office.

Applicant believes no fee is due with this response. However, if a fee is due, please charge our Deposit Account No. 50-0591, under Order No. 02008/071003 from which the undersigned is authorized to draw.

Dated: January 20, 2006

Respectfully submitted,

By 

Thomas K. Scherer

Registration No.: 45,079

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